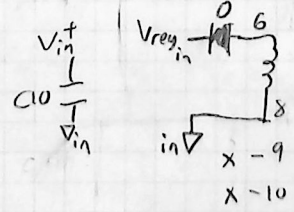
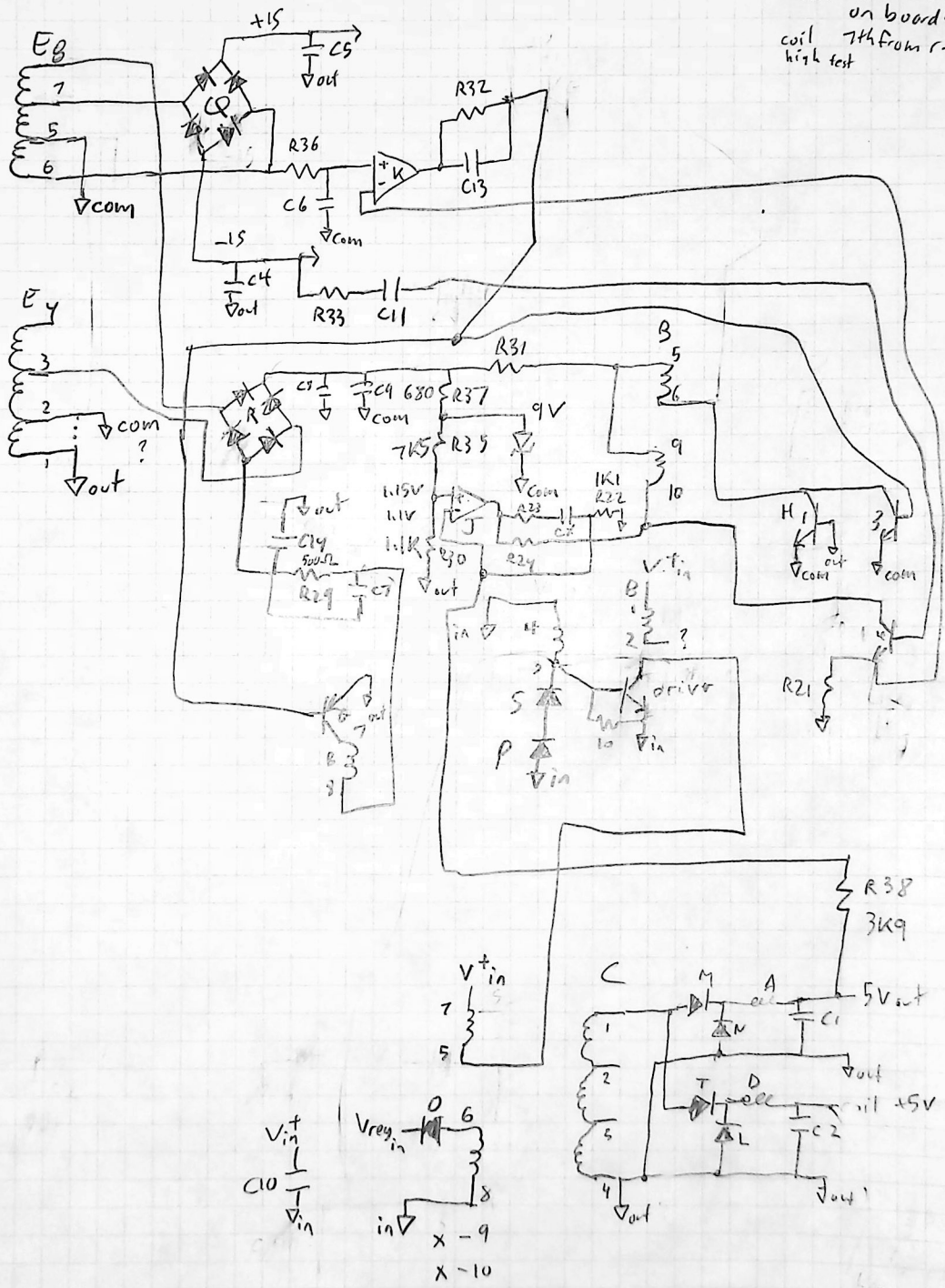
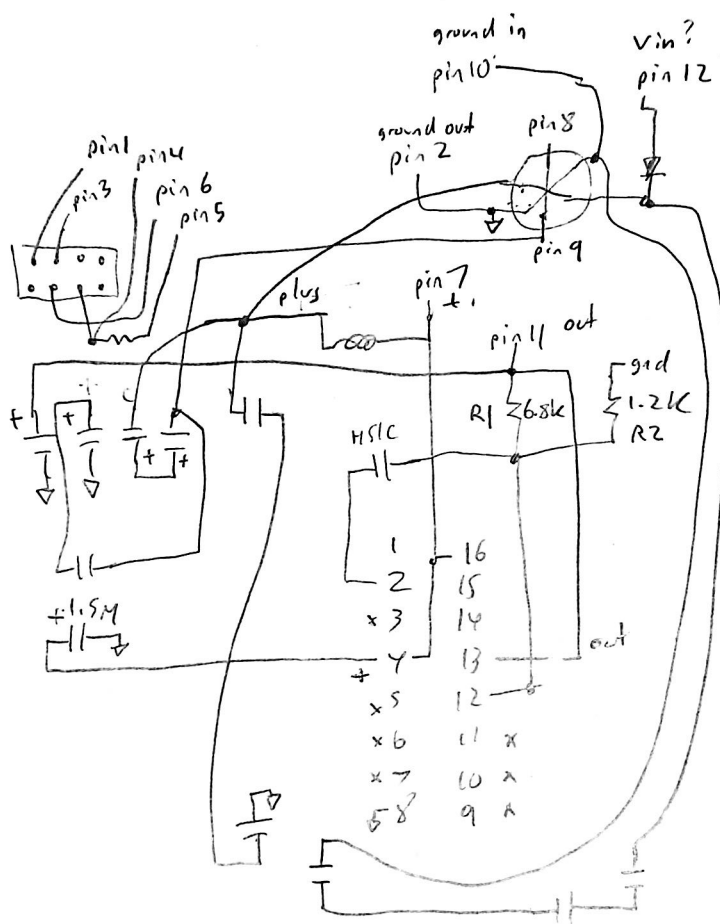


Board 1
 relay to pins 8,
 coil: 4th from rt
 on board 4
 coil 7th from rt
 high test

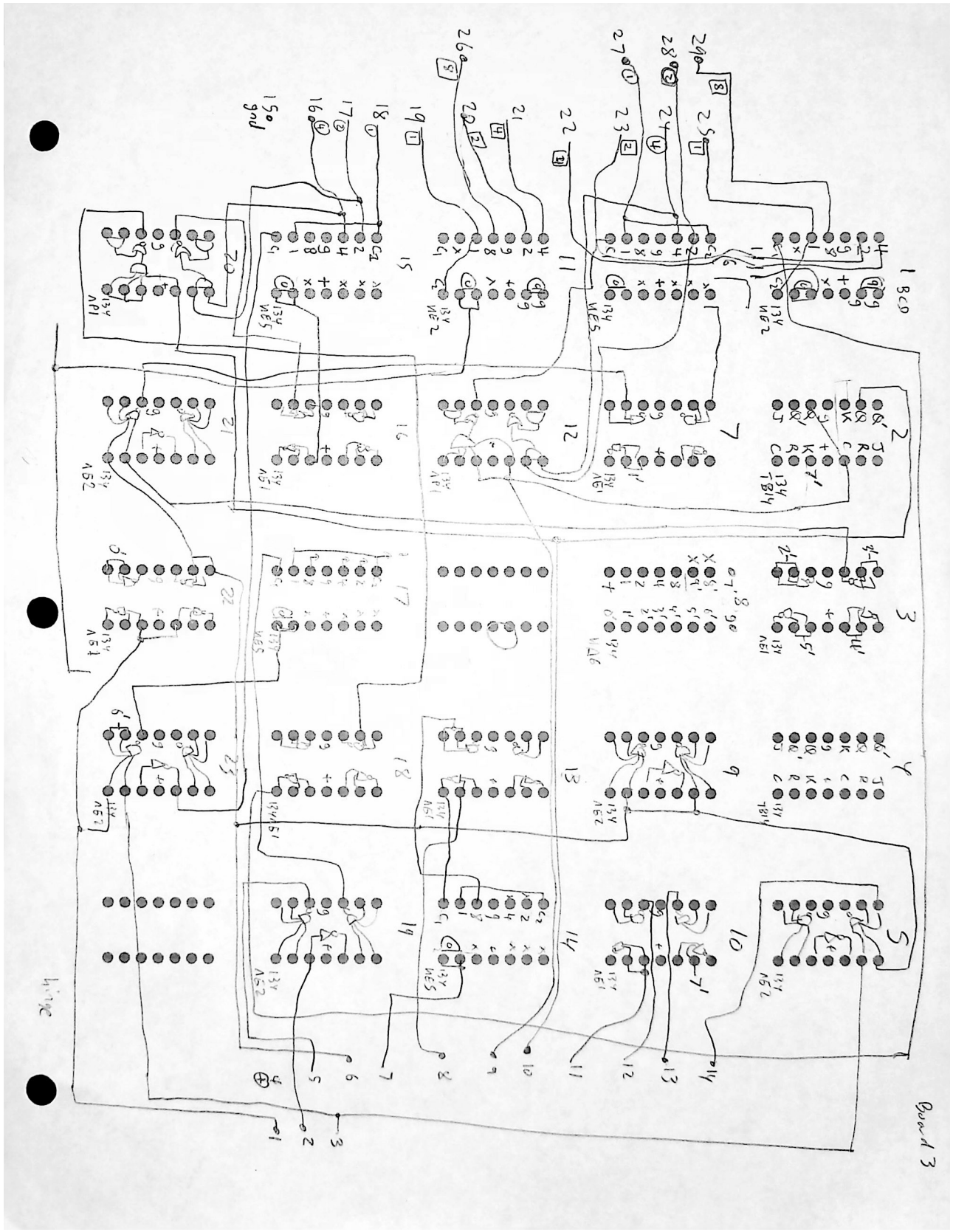


B
 B
 C



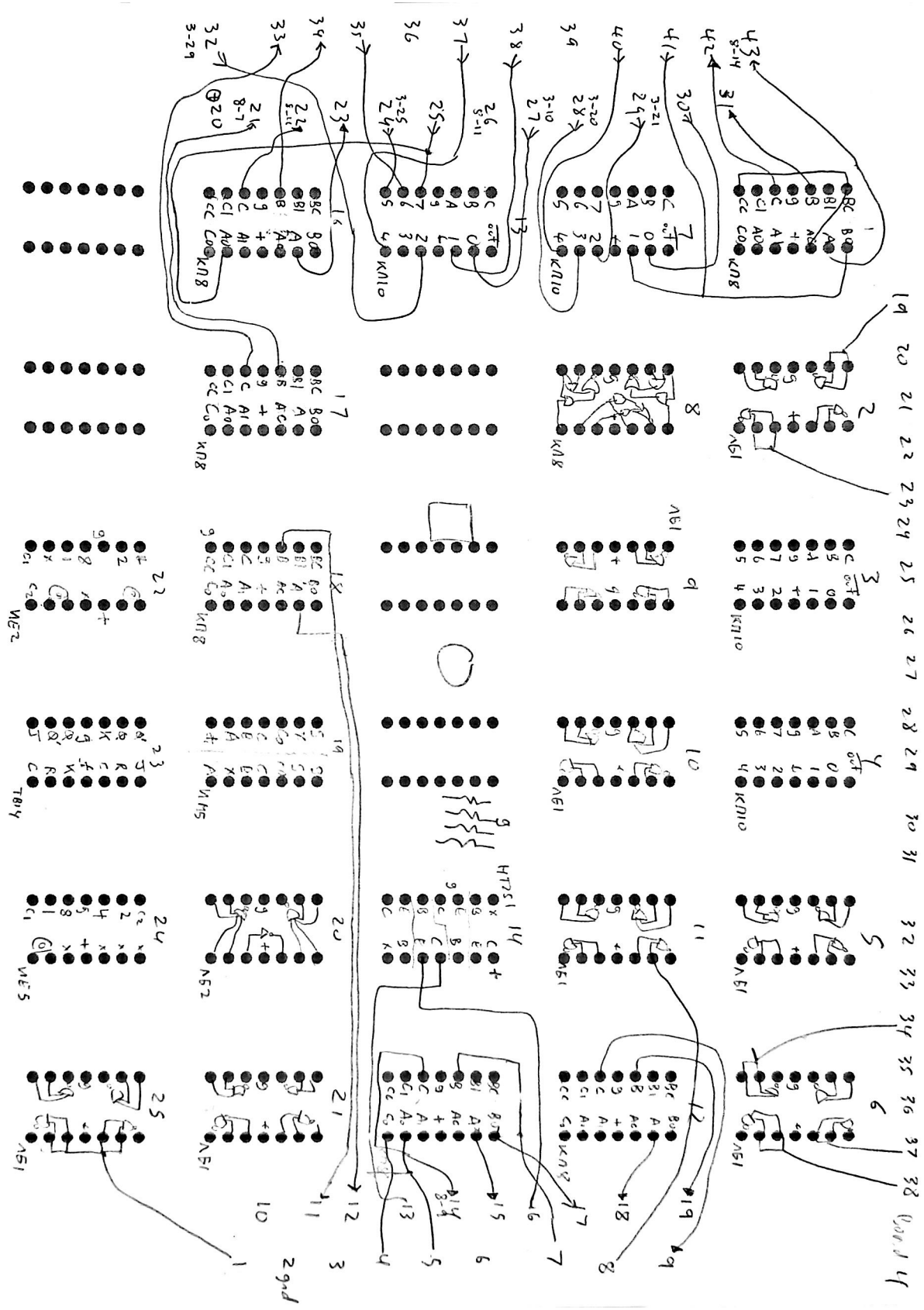
Power board 2
 50422 clock
 conn 1 = ground
 conn 15 = 1 = conn 3
 pin 8, 9

1	16	
2	15	
x 3	14	
+	13	out
x 5	12	
x 6	11	x
x 7	10	x
5 8	9	x

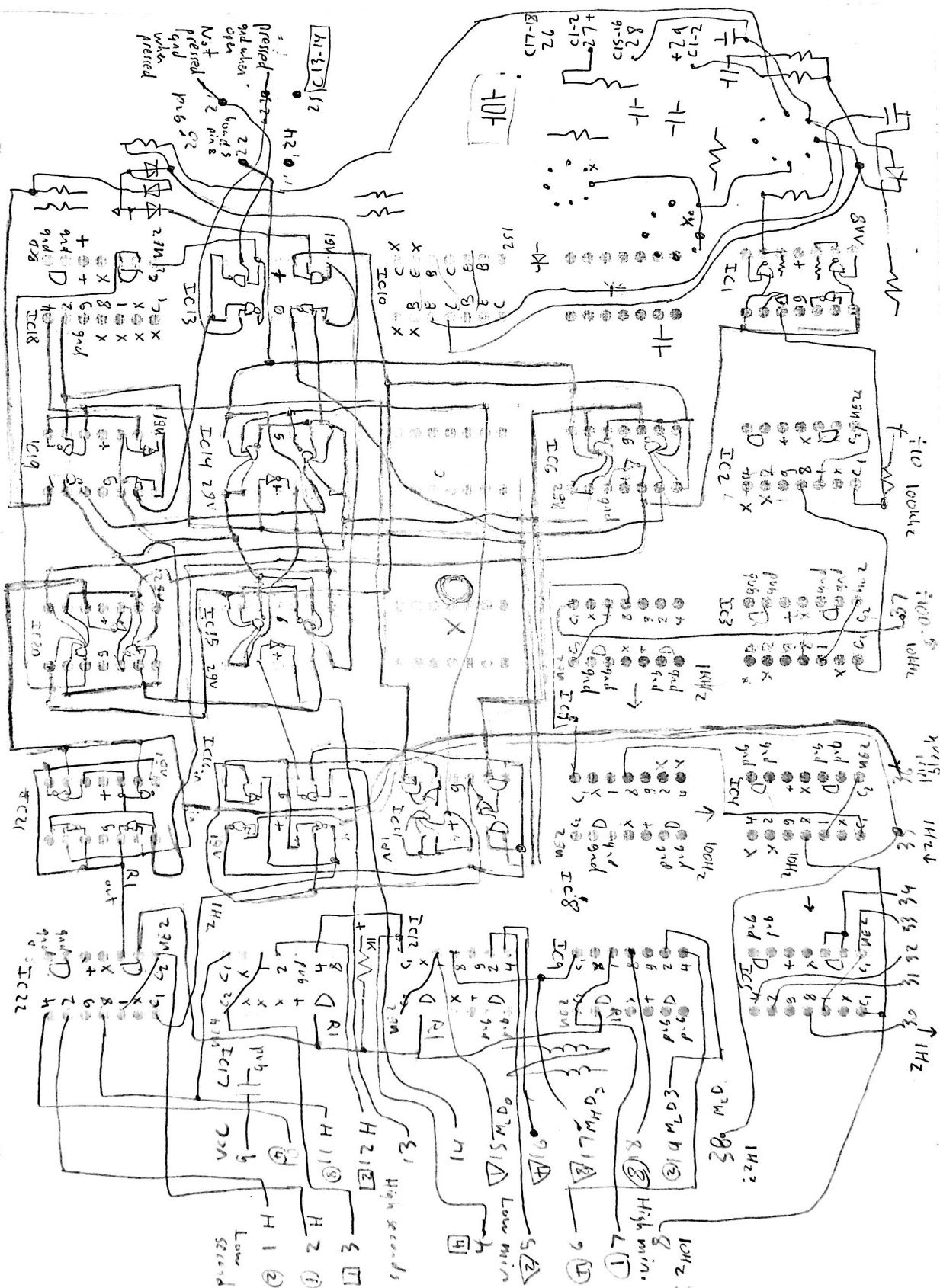


Board 3

Living

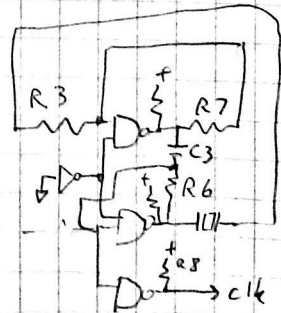
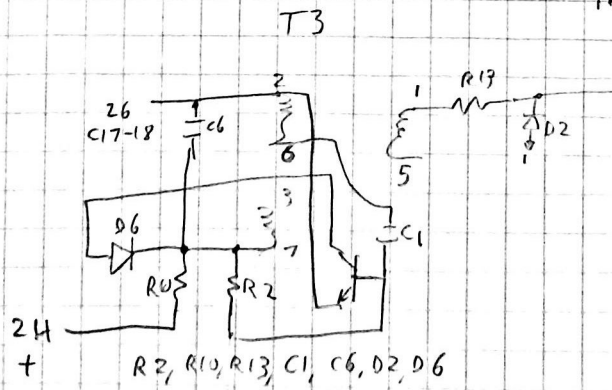
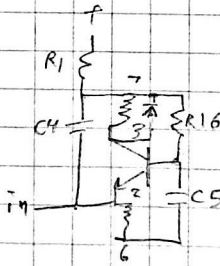
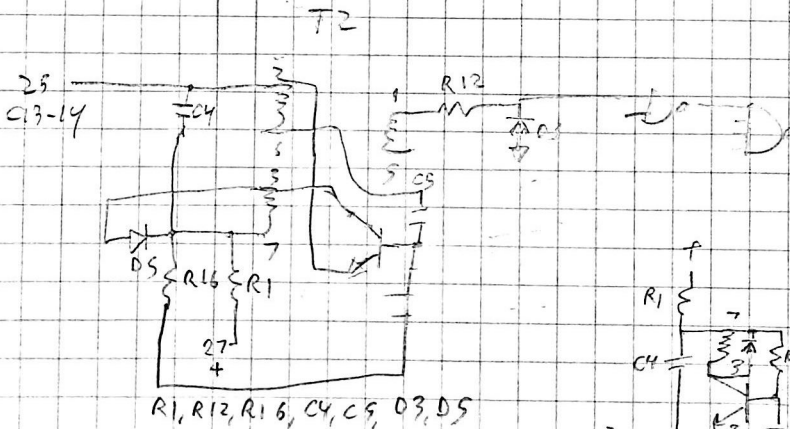
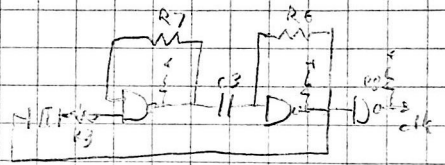
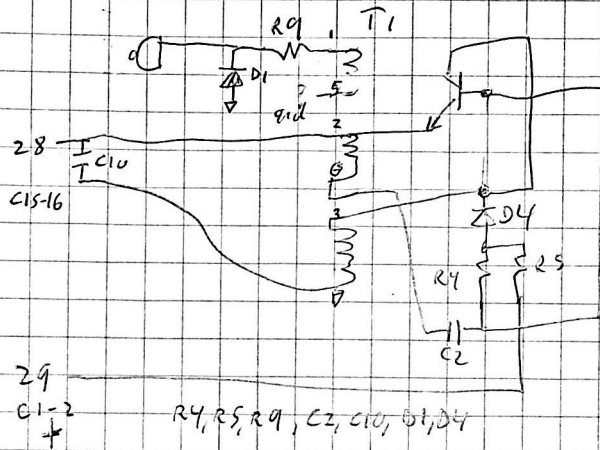


19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 (continued)



Feed on both high count on low clock

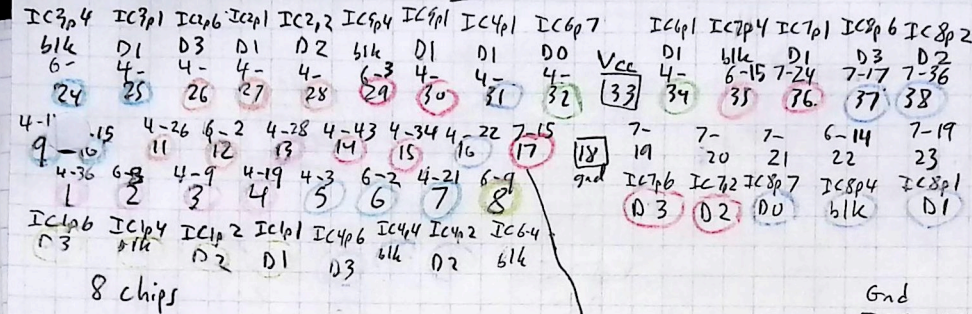
Board 7
Transformers,
oscillator



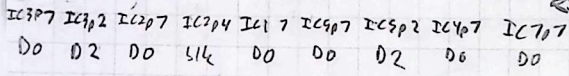
to do: R3, R6, R7, R8, R11, R12, R13, C3, C4, C5, C6

Driver

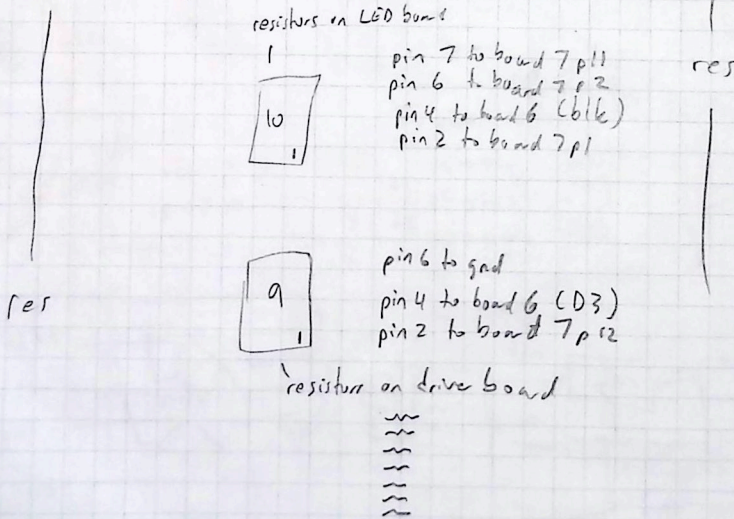
Board 8



Gnd
 IC6: p2, 6 D2, D3
 IC3: p 6 D3
 IC5: p 6 D3



Driver 2 - res



1: 13, 12, 11, 10, 9, 8

7, 6, 5, 4, 3, 2

6:
GND ← 1, 6
8-12 2, 7 → 8-6
8-2 3, 8
4, 9 → 8-8
5
24

10 → VCC
11 conn 9
12 driver
13 conn 5
14 8-22
15 8-35

2: 3, 4, 5, 6, 7, 8, 9, 10, 11, 12
1
2

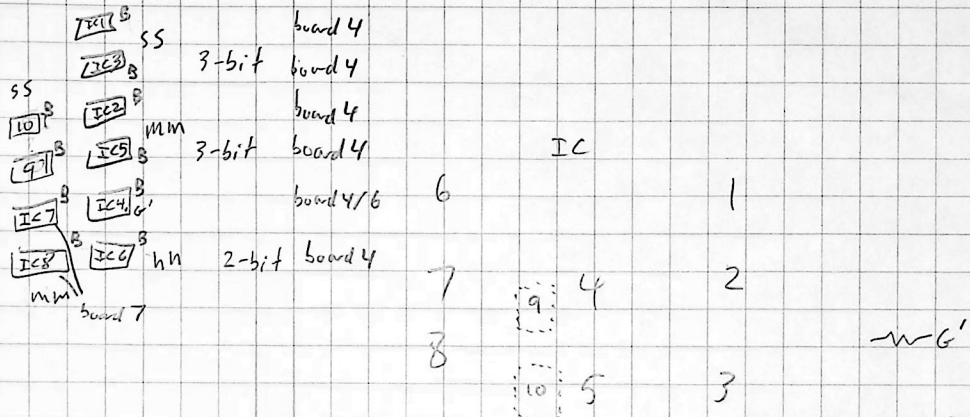
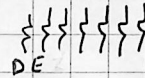
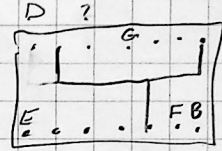
3: 5-3 → 1, 4 → VCC, 15 → GND
5-13 ← 2, 5 → 4-1, 16 → 4-5
5-6 → 3, 6 → 5-7, 17 → 4-17
7 → 5-12, 8 → 7-8, 18 → 4-4
9 → 4-38, 4-40 ← 19, 26 → 4-41
10 → 4-27, 4-29 ← 20, 20
11 → 7-13, 4-6 ← 22
12 → 5-18, 4-37 ← 23, 27
13 → 5-15, 4-35 ← 24, 28 → 8-13
14 → 4-10, 5-17, 4-34 ← 25, 29 → 8-32

7:
9 → VCC, GND ← 20, 23
10, 21
11, 22, 24
12, 25
13 → 3-11
14
15 → 8-17, 26
16, 27
17, 28
18, 29
19
30-37

4:
5-5 → 1, VCC ← 20, 32 → 8-27
GND ← 2, 10 → 3-14, 5-17, 6-24, 8-7 ← 21, 33
18-5 ← 3, 8-16 ← 22, 34 → 8-15
3-18 → 4, 23
3-16 → 5, 7-25 ← 24, 35 → 3-26
17-20 → 6, 14 → 8-9, 7-25 ← 24, 25, 36 → 8-1
15 → 8-10, 8-11, 26, 37 → 3-23
3-6 → 7, 3-10, 27, 38 → 3-9
5-9 → 8, 17 → 3-17, 34
18, 3-20 ← 28, 40 → 3-19
19 → 8-4, 3-21 ← 29, 41 → 3-26
44-63 on bottom unnumbered, 30, 42, 31, 43 → 8-14

5:
1, 15 → 3-13
2 → VCC, 16 → GND
3, 17 → 3-14, 4-10, 6-24
4, 18 → 3-12
5, 10
6, 11
7-3 → 6, 12 → 3-7
13 → 3-2
7, 14
3-1 ← 8
4-8 → 9, 19-38 on bottom

Display



col 2 top = common : board 4
 = board 1 pin 5V (5V2)
 col 2 p2: other chip?

